



July 2019

### Quick Links

- [Automated Test Equipment](#)
- [Simulation Systems](#)
- [Battery Test and Simulation](#)
- [Speak with an Engineer](#)

### Upcoming Events



NI Aerospace and Defense Forum  
August 14

THE **BATTERY** SHOW  
NORTH AMERICA 2019

The Battery Show  
September 10-12

#### [Events](#)

### We're Hiring!

Senior Southeast US Sales Engineer  
Senior ATE Systems Engineer  
LabVIEW Systems Engineer

#### [Careers](#)

### LabVIEW Enthusiasts



Bloomy hosts LabVIEW and TestStand training classes at our training facilities in CT, MA and NJ.

#### [Training Schedule](#)



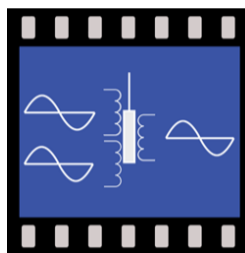
Interact with fellow LabVIEW enthusiasts to network, share coding practices, and discuss measurement and automation applications.

#### [Mass Compilers](#)



### Bloomy Announces Five SLSC Modules for Aerospace HIL Simulation Testing

New Switch, Load, and Signal Conditioning (SLSC) modules and accessories enable aerospace, defense, automotive and other industries perform hardware-in-the-loop (HIL) simulations for testing complex electronic control systems.



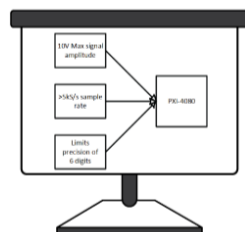
### SLSC Sensor Simulations for HIL in Mil-Aero Systems

Switch load and signal conditioning (SLSC) architecture offers a new solution that makes commercial off-the-shelf simulations feasible.



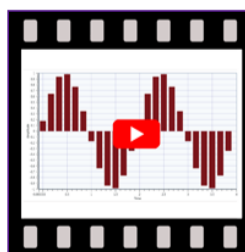
### Approaching Current Challenges with BMS Testing

Learn about the greatest challenges facing battery management systems, and what it might take for all vehicles to become fully electric in this key speaker interview with Bloomy's President Peter Blume.



### Automating Test Procedure to Test Sequence with the TestStand API

Do you ever wish that TestStand would write itself? This presentation explains how Bloomy created a tool that automatically turns test definition spreadsheets into TestStand sequence files by leveraging the TestStand API in LabVIEW.



### Running in Circles - Digital Pattern Generation

The objective of Direct Digital Synthesis (DDS) is to provide a highly flexible and accurate analog/digital output generation mechanism. This video explores implementing your own DDS engine in FPGA.



### Valuable Resources at Your Fingertips

Create an account on **Bloomy.com** and gain access to valuable resources including downloads associated with several of the articles in this newsletter. **REGISTER TODAY!**